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ABSTRACT

There exists a demand for radiation-safe and high-speed communication systems available to public users in the fifth-generation (5G) communication and beyond. In this regard, visible light communication (VLC) stands out offering multi-Gigabit-per-second (Gbit/s) data transmission, energy efficiency and illumination, while being free from electromagnetic interference. Here, we report a high-speed VLC link by using a 443-nm GaN-based superluminescent diode (SLD) and bit-loading discrete-multiple-tone (DMT) modulation. Analysis of the device characteristics and modulation parameters shows a feasible bit allocation of up to 256-QAM while obtaining up to 3.8 Gbit/s data rate. These results, together with the electro-optical properties of the SLD such as being droop-free, speckle-free and high-power, make it an attractive solution for the future of public communications and smart lighting, while complementing traditional fiber-based and millimeter-wave technology.

Keywords: Visible light communication (VLC), superluminescent diode (SLD), Gallium Nitride (GaN).

1. INTRODUCTION

The ultra-low-latency network beyond the fifth-generation (5G) will demand multi-Gigabit-per-second (Gbit/s) communication systems available to public users1 without the concerns of excessive radiation exposure.2,3 Under this context, visible light communication (VLC) have the potential to complement existing technologies by offering high-speed data transmission, energy efficiency, and integration to white lighting, while being free of electromagnetic interference.4 Up to date, a vast majority of the high-speed VLC systems have been demonstrated using light-emitting diode (LED)5–8 and laser diode (LD).9–11 A novel device combining advantages of both LD and LED, named superluminescent diode (SLD) has been explored towards VLC only on few occasions by our group,12–15 however, additional investigation and development are needed to develop the full potential of SLDs towards high-speed VLC.

Visible light SLDs have shown attractive properties to a number of applications such as optical coherence tomography (OCT)16 and augmented reality displays17. This is because of the SLD’s unique combination of high-power, droop-free operation, low enduse, low temporal coherence, among others. One of the important applications of SLDs18, is their application to dual illumination and communication19, hence, the further improvement and research of SLDs towards VLC and lighting are important for technology and commercial endeavors.

The use of advanced modulation in VLC is set as the avenue for current development towards higher communication speeds. Modulation schemes with high spectrum efficiency have been proposed for VLC applications such as discrete-multiple-tone (DMT) modulation technique.6,19. Such techniques maximize the data rate within a restricted power and bandwidth budget of the system. Additionally, equalization and bit-loading techniques enhance the optical system complementing the DMT scheme20.

Here, we report on the implementation of spectrally efficient DMT modulation applied to an SLD emitting at 443 nm with an optical bandwidth of ~4.3 nm and an instantaneous peak optical power of 128 mW. A data rate of 3.8 Gbit/s with a bit
error ratio (BER) of 3.8 $E^{-3}$ is demonstrated using bit-loading-DMT modulation scheme with post-equalization over a 30-cm free-space VLC channel for proof of concept. The achieved data rate relies on a device frequency bandwidth of up to 900 MHz, a system frequency bandwidth of 750 MHz, and a maximum quadrature amplitude modulation (QAM) order of 8, leading to up to 256-QAM. By combining the optical characteristics of the SLD together with a spectrally efficient modulation scheme, we present a unique system that has the potential of being integrated into high-speed and user-oriented communication deployments in the future.

2. PRINCIPLE

2.1 Superluminescent diode (SLD)

A blue SLD was fabricated in a commercial c-plane group-III-Nitride-based laser diode structure by etching a $\sim 12^\circ$ tilted front facet (Fig. 1(a)). The device depicts a ridge waveguide with a length of $\sim 1$ mm and a width of 15 µm. The $12^\circ$ tilted facet is chosen to reduce the reflectivity of the front facet and avoid lasing action, whereas the back facet is coated with a high reflective mirror following the design of a double-pass SLD reported by Alphonse\textsuperscript{21}. Figure 1(b) shows the light-output-power – current – voltage (L–I–V) characteristics of the SLD as measured in continuous wave (CW) injection current with a laser diode test system (Keithley 2520) and a calibrated silicon photodetector in an integrating sphere (Labsphere). The device is mounted in a thermoelectric cooler (TEC, SaNoor-SN-LDM-T) to maintain a constant case temperature of 16 °C.

The I–V curve with a turn-on voltage of $\sim 3.5$ V shows an almost linear I–V regime from 500 mA onwards (Fig. 1(b), black line), this linearity appeals potential for device modulation. The L–I curve (Fig. 1(b), blue line) increases exponentially due to the amplified spontaneous emission (ASE), leading to a droop-free performance as seen from the external quantum efficiency (EQE) plotted as a dashed line in Fig. 1(b). The instantaneous output power shows a maximum value of 128 mW as the current is increased to 1000 mA ($\sim 6.7$ kA/cm$^2$).

![Image of SLD device and its electro-optical characteristics](image-url)

Figure 1. The SLD device and its electro-optical characteristics: (a) Scanning electron microscope image of the SLD showing the metal contact (p-side and n-side), the 15 µm ridge-waveguide, and the $12^\circ$ tilted front facet. (b) Instantaneous light-output-power – current – voltage (L–I–V) characteristics and calculated external quantum efficiency of the SLD (CW). (c) Electroluminescence (EL) spectra of the SLD under different CW injection current (100mA – 1000mA).
The electroluminescence (EL) spectra characterized with an optical spectrum analyzer (Yokogawa AQ6373B) depicts characteristic SLD behavior as shown in Fig. 1(c). The optical bandwidth measured as the full width at half maximum (FWHM) decreases from 11 nm (100 mA) to 7.8 nm (200 mA), followed by an FWHM of 6.9 nm (300 mA) down to 5 nm (900 mA) and 4.3 nm (1000 mA). Here, we can attribute the optical bandwidth narrowing to the ASE due to selective light amplification at the gain peak in the active region. Indeed, the ASE can be observed in Fig. 1(c) under injection currents as low as 100 mA (identified as the dominant peak at 445 nm). The initial peak position located at 445 nm shows a relatively-small blue-shift to 442.3 nm (700 mA) which is expected from the screening of the quantum-confined Stark effect at higher currents\(^{22}\) and the band-filling effect. The peak position increases again up to 443.3 nm (1000 mA) due to self-heating in the device created by the higher CW injection current. Starting from 700 mA, noticeable spectral distortions occur near the peak emission, these distortions are likely due to partial modal reflection at the front facet creating cavity modes\(^{22}\). These are undesirable for certain applications such as the OCT, and are expected to be eliminated by using a different device design or an anti-reflection (AR) coating in the front facet, nonetheless, the AR coating could potentially lead to a lower output power\(^{21}\) which is important for lighting and displays applications.

2.1.1 Frequency response

The frequency response of the SLD in the range from 10 MHz to 2 GHz was measured using a network analyzer (NA, Agilent E8361C PNA) and the result is shown in Fig. 2, where a maximum frequency response of ~600 MHz at -3 dB, and ~900 MHz at -10 dB are attained. The input signal of the SLD is generated from the NA and combined with the direct current (DC) bias from the power source (Keithley 2400) by using a bias-tee (Tektronix PSPL5580), while keeping the device in the TEC. The SLD’s output light was collimated with a lens and projected over 30 cm to another lens focusing the light into an avalanche photodetector (APD, MenloSystem-APD210). The system is calibrated with an E-Cal module (Agilent 85093-60010).

![Figure 2. Frequency response of the SLD under various injection current showing a frequency bandwidth of up to ~600 MHz at –3 dB and up to ~900 MHz at -10 dB.](image)

2.2 Bit-Loading Discrete-Multiple-Tone Modulation Scheme

In order to take advantage of the full frequency resource of SLD, bit-loading-DMT modulation scheme is proposed to maximize the spectral efficiency. Bit-loading-DMT modulation scheme is a widely used and adaptive multi-carrier modulation technique which divides the frequency spectrum into multiple communication channels with a certain signal-to-noise ratio (SNR). Considering the BER performance and the SNR, the suitable number of bits per symbol is assigned within each channel, which is also called bit-loading allocation. To modify the overall BER performance, we further shave some peak value of allocated QAM order, which builds bit-loading allocation with the shape of decreasing step and smooth transition. A typical bit-loading-DMT modulation schematic is shown in Fig. 3. Assuming that the length of the fast Fourier transform (FFT) is \(N\), the generated bit-loading DMT signal \(S_b\) of time \(b\)-th is defined as:

\[
S_b = \sum_{a=0}^{N-1} S_{ba} e^{j2\pi \frac{b}{N}}
\]  

(1)
where $S_a$ is the $M$-QAM symbol on the $a$-th subcarrier. In detail, its distribution satisfies the Hermitian symmetry and its value depends on the SNR estimation of the certain channel.

### 3. EXPERIMENTAL SETUP

Figure 3 shows the experimental set up of the high-speed SLD-VLC system. The Pseudo-Random Binary Sequence (PRBS) of the signal was generated by MATLAB software and operated by the bit-loading allocation. The order of QAM mapping of each channel was decided by SNR estimation. Naturally, a higher SNR could accommodate a greater QAM order. Besides this, the signal needed zero-padding to avoid the negative effect of signal-signal beat interference (SSBI) and low-frequency noise after serial-parallel transformation. Then, the bit-loading-DMT signal was generated after Hermitian symmetry, up-sampling, inverse-FFT, adding the cyclic prefix (CP) and the parallel-serial transformation. The generated signal was loaded into an arbitrary waveform generator (Tektronix-AWG70002A). Afterward, the electric signal was amplified by an electronic amplifier (MiniCircuits-ZHL-6A-S+) and coupled with DC bias by the bias-tee (Tektronix-PSPL5580) to drive the SLD. A TEC was used to avoid overheating of the SLD and to keep the output power constant. Collimated by two lenses and using a Neutral-Density (ND) filter, the light was received by the APD and resampled by an oscilloscope (Tektronix-DPO72004C) to do further offline signal processing.

At the receiver side, the resampled signal was transformed to the signal in the frequency domain by down-sampling and FFT. After that, Intra-symbol frequency-domain averaging (ISFA) was used to smoothing the channel and assist to do zero-forcing post-equalization to mitigate the linear noise. At last, we applied parallel-series transformation and QAM demodulation. It is important to mention that a binary phase-shift keying (BPSK)-DMT signal should be transmitted first to characterize the channel and get the SNR estimation.

![Figure 3. Upper: The schematic of bit-loading-DMT. Lower: Experimental set up of the VLC system based on SLD.](image)

### 4. RESULTS AND DISCUSSION

For VLC, choosing a suitable value of injection current is a critical step to get the best electro-optical conversion and avoid the signal distortion. Thus, by using the bit-loading DMT scheme, we first measured the achievable data rate and BER performance under different injection current, with a reference fixed system channel bandwidth of 500 MHz and a signal...
peak-to-peak voltage (V_{pp}) of 0.5 V. As shown in Fig. 4(a), the data rate increases from 1.53 Gbit/s to 2.67 Gbit/s when the current goes from 100 mA to 800 mA. At 900 mA, a lower data rate of 2.41 Gbit/s is at the same level as that of 700 mA (2.42 Gbit/s), therefore, injection currents below 800 mA are preferred. It is important to highlight that having over 1.5 Gbit/s at a low injection current of 100 mA, indicates the feasibility of having Gbit/s-class communication at longer distances or in a scheme with reduced energy-budget.

Next, we analyzed the system data rate and BER for a varying V_{pp}, a fixed injection current of 700 mA, and bandwidth of 500 MHz (Fig. 4(b)). When the V_{pp} of the signal is growing from 0.25 V to 0.4 V, the achievable data rate increases and the BER decreases. This is because the SNR of the system increases as V_{pp} is increasing. After 0.4 V, the data rate is decreased with a slightly higher BER. This is attributed to a bias point out of the optimal point, leading to non-linearities, distortion in the received signal, and decreased SNR. Thus, we select 0.4 V as the V_{pp} value for further experimentation. In summary, the best values for biasing the SLD were determined to be ~800 mA and ~0.4 V (V_{pp}).

We carried out experiments to explore the maximum achievable data rate under different frequency bandwidths with a fixed injection current of 800 mA and a V_{pp} of 0.4 V. As seen in Fig. 5, the highest data rate of 3.8 Gbit/s with a BER of 3.8 E-3 is obtained when the bandwidth is set to 750 MHz.
Figure 5. Achievable data rate and relative BER performance of the SLD versus different frequency bandwidth of bit loading-DMT signal.

Figure 6. (a) Bit allocation and BER results with the corresponding performance of the adaptively QAM constellation. (b)(c) The frequency response and bit allocation at the highest data rate of 3.8 Gbit/s. Constellation maps of (i) 256QAM, (ii) 128QAM, (iii) 64QAM, (iv) 32QAM (v) 16QAM (vi) 8QAM (vii) 4QAM (viii) BPSK at the highest data rate of 3.8 Gbit/s.

The description of the bit-loading allocation at the highest data rate of 3.8 Gbit/s is discussed in Fig.6. To adaptively deploy the suitable bit-loading allocation for the VLC channel, a series of DMT-4-QAM pilot signal is transmitted to measure the SNR of the channel. Through calculating the error vector magnitude (EVM) of the received signal in every subcarrier, the SNR response in the frequency domain can be estimated and depicted in Fig.6 (b). The theoretically maximum spectral
capacity of every subcarrier can also be obtained from the estimated SNR in their own frequency range and the desired BER at 3.8 E-3 based on Equation 13 in Ref. 24. Thus, the value of the QAM order (allocated bits) for every subcarrier is the closest and smaller integer value to the theoretically maximum spectral capacity, shown in Fig.6 (a). To further prove the error-free transmission at the highest data rate, the constellation diagrams with different QAM orders are given in Fig. 6 (b), on which every constellation cluster can be clearly distinguished without severe overlapping problem. At the low-frequency range, the high SNR can support up to 256-QAM signal transmission and the lowest QAM order is BSPK at the high-frequency range around 750 MHz, which means the channel has been fully exploited.

5. CONCLUSIONS

Gearing towards the potential implementation of a light-based multi-Gbit/s system available to public users in the future communication network, in this paper, we demonstrate a 3.8-Gbit/s VLC system based on a blue SLD with bit-loading-DMT modulation scheme. We presented the optical properties of the SLD, including high-power, broadband and droop-free emission, as well as the optimization of the bias point, $V_{pp}$, and frequency bandwidth for the communication system. The number of bits per symbol of QAM in the lower-order subcarriers is up to 8 (i.e. 256QAM) while fully utilizing the 750 MHz bandwidth of the system. These results demonstrate the efficient application of bit-loading methodology to SLD-based VLC and set a further step into the future deployment of SLDs inside the illumination and communication technology environment.

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