PARALLEL LOGICS USING MULTIMODE EXCITATION OF A SINGLE MEMS RESONATOR

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ABSTRACT
Performing Boolean operations using conventional CMOS-based computers requires the wiring of multiple transistors. Hence, the processors of these computers are composed of millions of transistors, which increase the device size and power consumption. Here, we present a single MEMS resonator that can execute in parallel multiple logic gates. The concept is based on simultaneously encoding the binary information at different modes of vibration of a microplate. The proposed novel method allows the device not only to perform as a fully integrated logic gate but also as a parallel logic processor for which the same device can perform multiple logic gates simultaneously. The proposed method decreases the device footprint and reduces the power consumption required to perform multiple Boolean functions.

KEYWORDS
Mechanical computations, parallel logic, multimode resonator, microplate.

INTRODUCTION
In traditional transistor-based computers, connecting different transistors is essential to execute logic operations, which increases the device size and energy consumption. Also, the transistor technology is reaching its physical limit. Hence, different alternatives are being sought to execute logic operations [1-6]. Motivated by their low power consumptions and robustness to electromagnetic shock, recently, micromechanical structures have been employed to demonstrate universal logic gates at which the computation can be carried out in two different forms, static and dynamic mode [1]. In the static mode, relays or MEMS switches are employed to execute the logic functions in which the logic states are realized by selectively actuating the switches. This mode has the advantages of zero leakage current and the ability to easily cascade multiple elements to execute complex logic operations. However, their high contact resistance, low speed, and stiction issues limit their potential in demonstrating practical applications [7, 8]. Therefore, a new research direction has emerged focusing on the noncontact mode by employing the dynamic vibration of MEMS and NEMS resonators. In the dynamic mode, the logic states are encoded by activating and deactivating the resonance frequency using different modulation and actuation techniques, such as DC tuning [9], electrothermal tuning [10, 11], parametric resonance [12], and frequency mixing [13]. Utilizing different modes of vibration to execute logic operations improves the operation speed of the logic unit and increases the lifetime of the proposed device. The enhanced functionality of the resonator-based computing has been demonstrated by showing a reconfigurable MEMS resonator that can perform several universal logic gates using a single MEMS resonator [10]. However, the difference between the input and output frequencies complicates the possibility of cascading multiple devices to perform complex logic functions. Recently, the potential for cascading multiple logic elements has been demonstrated by operating different resonators at the same frequency and using the AC signal to represent the logic inputs [14]. Multiple logic gates are demonstrated by selectively activating and deactivating different modes of vibrations of a piezoelectric circular membrane [15]. Also, by utilizing parametric resonance, the possibility of realizing parallel logic is demonstrated [3]. However, the need for extremely controlled test conditions and the small frequency difference between the parallel logic outputs (~1 Hz) hinder their potential in practical application.

Recently, exploiting information from the different modes of vibration of a single MEMS resonator has gained significant attention in wide range of applications, such as the simultaneous measurements of temperature and humidity using two modes [16], extracting the mechanical properties and mass of adsorbed particles [17], determining the position and mass of adsorbed analytes [18, 19], and demonstrating a smart sensor that can autonomously activate a switch upon exceeding a threshold value [20].

Here, we demonstrate an electrostatically actuated microplate resonator, simultaneously actuated near the first four modes of vibration, which can perform various logic gates in parallel (simultaneously). Each gate is operated at a single mode. Hence, by using the appropriate electrical interconnect, multiple devices can be cascaded to demonstrate complex logic operations.

EXPERIMENTAL SETUP
As shown in Fig. 1a, the microplate, which forms the upper electrode, is fabricated from a 4.3 \( \mu m \) polyimide layer coated with metals from top and bottom. The upper electrode is perforated to reduce the effect of squeeze film damping and decrease the time required to fully release the device. The microplate length \( (l) \) is 400 \( \mu m \) and of width \( (w) \) 150 \( \mu m \). The perforation diameter \( (d) \) is 60 \( \mu m \) with spacing \( (S) \) of 50 \( \mu m \). The lower electrode is divided into six different electrodes that can be separately accessed. By powering specific lower electrode configuration, we can selectively activate and deactivate particular modes of vibration. The experimental setup is shown in Fig. 1b. The microbeam is electrostatically actuated by a data acquisition card that provides a multi-
frequency signal. A laser Doppler vibrometer is utilized to monitor the beam response.

![Figure 1: (a) Schematic of the microplate with lower partial electrodes. (b) The experimental setup showing the microplate, data acquisition card, and the four input switches used to provide the logic inputs.](image)

<table>
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<th>Color</th>
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<th>Thickness (nm)</th>
<th>Modulus Of Elasticity (GPa)</th>
<th>Density (kg/m³)</th>
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<td>85</td>
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<td>Chrome (Cr)</td>
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<td>279</td>
<td>7190</td>
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<td>19300</td>
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<tr>
<td>Red</td>
<td>Polyimide (PI)</td>
<td>4300</td>
<td>8.5</td>
<td>1400</td>
</tr>
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</table>

**RESULTS AND DISCUSSION**

![Figure 2: Frequency response of the microplate to a white noise excitation at \( V_{DC} = 10 \text{ V} \), \( V_{AC} = 15 \text{ V} \), and at a chamber pressure \( P = 4 \text{ mTorr} \). Insets: the corresponding mode shapes acquired by recording the response at different points along the microplate surface using a laser Doppler vibrometer.](image)

![Figure 3: Modes activation and deactivation by powering different lower electrode configuration to a white noise excitation at \( V_{DC} = 10 \text{ V} \), \( V_{AC} = 15 \text{ V} \), and at a chamber pressure \( P = 4 \text{ mTorr} \). (a) One electrode. (b) Two-side electrodes. (c) Two-anchor electrodes. (d) Two-diagonal electrodes. (e) Three electrodes. (f) Four electrodes.](image)

The resonance frequencies and their corresponding mode shapes are revealed by using one electrode to actuate the microplate with white noise signal while recording the vibrations at different points along the microplate surface, Fig. 2. As shown in Fig.2, the first six modes can be excited using any of the corner electrodes. In this study, we utilize the corner electrodes to selectively activate the first four modes. To demonstrate the concept, we powered different lower electrode configuration with the white noise signal and recorded the microplate response, Fig.3. As shown in Fig. 3a and 3e, the four modes can be excited using one electrode or three electrodes. By powering the side electrodes, Fig. 3b, the first and second modes are activated. Connecting the two-anchor electrodes activates the first and third mode, Fig. 3c. Figure 3d shows that the first and fourth mode can be activated by powering the diagonal electrodes. As depicted in Fig. 3e, powering the four electrodes activates the first mode only. Hence, by powering different lower electrodes, which represent the logic inputs, certain modes can be selectively activated/deactivated, which represents the logic output.

Figure 4 shows the results of sweeping the frequency response near the first four modes of vibration. This test is important in determining the appropriate voltage required to activate the different modes with a high signal-to-noise ratio. To demonstrate the parallel logic operation, we utilize the four corner electrodes to actuate the microplate with a multi-frequency electrical signal that has frequencies matching the first four modes. Then, to realize the different possible logic gates, we monitor the response to different input combinations by connecting and disconnecting the input switches shown in Fig. 1b. The results are demonstrated in Fig. 4 for the 16 possible input combinations. For example, by considering Fig. 4a, we demonstrate the possibility of realizing OR gates at \( \omega_{11} \) and \( \omega_{12} \), and also XOR gates at \( \omega_{21} \) and \( \omega_{22} \). The logic low state ‘0’ is defined for vibration amplitudes less than 10 nm, and the logic high state ‘1’ is defined for any response above 10 nm. The reported results show the possibility of realizing XNOR and NOT gates in parallel as shown in Fig. 4b and Fig. 4c. Figure 4d shows the potential of performing NAND and two XOR gates simultaneously.
Figure 4: frequency response of the microplate at $V_{DC} = 5\, V$ and a chamber pressure $P = 4\, \text{mTorr}$ near the different modes of vibration. (a) The first mode $\omega_{11}$. (b) The second mode $\omega_{12}$. (c) The third mode $\omega_{21}$. (d) The fourth mode $\omega_{22}$.

Figure 4: The response of the microplate to a multi-frequency excitation near the first four modes of vibration and different logical input configurations. (a) OR gates are demonstrated at $\omega_{11}$ and $\omega_{12}$, and XOR gates are demonstrated at $\omega_{12}$. (b) XNOR gate is demonstrated at $\omega_{12}$ in parallel with a NOT gate at $\omega_{21}$. (c) XNOR gate is demonstrated at $\omega_{12}$ in parallel with a NOT gate at $\omega_{22}$. (d) NAND gate is shown at $\omega_{12}$ while XOR gates are shown at $\omega_{21}$ and $\omega_{22}$.

CONCLUSIONS

We presented a single device that can simultaneously execute fundamental logic operations. The concept is based on selectively activating and deactivating different mode vibration of a microplate, which represents the logic output, by powering different lower electrodes configurations, which represent the logic inputs. The results show the possibility of simultaneously realizing OR and XOR, XNOR and Inverter, and NAND and XOR gates. The proposed method increases the integration density and reduces the power consumption required to perform multiple Boolean functions. Further research in this device can be directed to develop the electrical read-out circuit and cascade multiple devices to execute complex logic operations.

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REFERENCES


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