Visualization of Charge Carrier Trapping in Silicon at the Atomic Surface Level Using 4D Electron Imaging

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ABSTRACT

The ultrathin thickness (~1-2 nm) of native oxide layer on silicon surfaces which acts as efficient trapping centers precludes the possibility of studying its impact on the surface-charge carrier dynamics by conventional time-resolved laser spectroscopic techniques due to the large penetration depth of the pump and probe pulses. Here, we use four-dimensional scanning ultrafast electron microscopy (4D S-UEM) with unique surface sensitivity to directly visualize the charge carrier dynamics on Si (100) crystals before and after surface treatment (that removes the native
oxide layer) in real space and time simultaneously. Our time-resolved snapshots of the top surface and Kelvin probe-force microscopy results demonstrate that the oxide layer can be formed within minutes after surface treatment, creating undesirable surface-trap states that destroy the population of photogenerated charge carriers on the surface and possibly at the device interface. This new surface observation provides critical photophysical insights into how a few atomic layers of oxide can dramatically influence charge carrier recombination dynamics in silicon solar cells.

**TOC Graphic**
Crystalline silicon (c-Si) is the most dominant material in the semiconductor and photovoltaics (PV) industry.\textsuperscript{1} Si holds a major market share of more than 90% thanks to its stability, abundance, nontoxicity and high photo conversion efficiency.\textsuperscript{2-6} To date, the record Si single-junction cell has reached an efficiency record of 26.7%\textsuperscript{7,8} to be compared to its theoretical limit of $\sim$ 29.4%.\textsuperscript{9} It is also a promising candidate for solar fuel generation, \textsuperscript{10-13} providing benchmarked efficiencies higher than 10%.\textsuperscript{14,15} Interfacial engineering is one of the few remaining frontiers for pushing the efficiency of Si solar cells closer to their theoretical efficiencies; as such it is essential to improve further our understanding of the fundamental photo physical processes at the nanoscale-surface level, which includes effects of light absorption, electron-hole separation and transport, and recombination through defects.\textsuperscript{3,16}

Probing the lifetime of photo generated carriers is one of the most sensitive methods to assess material quality and holds direct relevance to device efficiency.\textsuperscript{16,17} For Si, due to its covalent nature, the surface often is the most important source of carrier recombination. Therefore, surface treatment and passivation have been essential preparation steps in the fabrication of all Si-based devices.\textsuperscript{18} The removal and protection of the surfaces from oxides, organic contamination and metallic impurities terminating the dangling bonds are crucial for achieving stability, reliability and high performance.\textsuperscript{18,19} The effective carrier lifetime ($\tau_{\text{eff}}$) depends on both the bulk lifetime ($\tau_{\text{bulk}}$) and surface lifetime ($\tau_{\text{surface}}$). Generally, the time scale of $\tau_{\text{bulk}}$ mainly depends on the concentrations of carriers and doped atoms.\textsuperscript{20} Although $\tau_{\text{bulk}}$ can be calculated by theoretical models\textsuperscript{20}, the results are usually higher than the $\tau_{\text{eff}}$ measured by relatively slow methods such as photoconductance techniques\textsuperscript{21} due to surface and interface carrier recombination.\textsuperscript{22} The differentiation between $\tau_{\text{bulk}}$ and $\tau_{\text{surface}}$ and how to directly measure them experimentally are among the largest challenges being faced by the materials science and physical chemistry
communities.\textsuperscript{22-25} There have been several reports describing methods to determine $\tau_{\text{bulk}}$ and $\tau_{\text{surface}}$ in Si materials.\textsuperscript{22,23,25,27-31} However, these approaches depend on mathematical models or only measuring $\tau_{\text{bulk}}$ by passivating the surface to eliminate surface carrier recombination and then calculate $\tau_{\text{surface}}$ with prior assumptions for the surface properties. In addition, the surface of silicon wafers controls the interface and contacting properties which often consist of stacks of different layers, often only a few nanometer thin, across which the transport of carriers occurs.\textsuperscript{23,24,26,32,33} Therefore, advanced techniques and strategies are urgently required to map the trap-densities, carrier lifetime and diffusion at the uppermost surfaces $\sim$ 1-5 nm, especially in applications where the size of devices is scaling down to thin films and nanostructures.

A native oxide layer is grown on Si surfaces at room temperature whenever they are exposed to an environment of oxygen and moisture, even for a very short period.\textsuperscript{19} The thickness of this native oxide on the Si surface has been determined to be 1-2 nm or even in the angstrom range (few monolayers).\textsuperscript{35-40} Despite its thin thickness, this native oxide layer, like other surface contaminants, affects the band structure and the electronic properties of the host materials as mentioned in several reports.\textsuperscript{26,27,35,41} Therefore, much attention has been given to characterizing and controlling this native oxide including the effects of different etchants.\textsuperscript{35-42} Hydrofluoric acid (HF)-based solutions are the etchants most widely used for cleaning Si prior to any processing step.\textsuperscript{43-46} The HF solution removes Si oxides from the surface and terminates dangling bonds with hydrogen atoms to act as a passivation layer.\textsuperscript{35,36,43,44} Despite the usefulness of the HF-based cleaning, several angstroms of native oxide regrow on the etched clean surface within minutes.\textsuperscript{35-37,40} This reoxidation process becomes slower with time until reaching a saturation thickness.\textsuperscript{35,36} The atmosphere of the environment, especially the degree of moisture, affects the regrowth rate of this native oxide layer on the etched clean surfaces.\textsuperscript{36} Few reports have studied the surface recombination in HF-etched
Si wafers using the optical pump-probe technique\textsuperscript{41} and lock-in carrierography.\textsuperscript{47} Sun \textit{et al.} showed that the surface recombination rate varies with the amount of dissolved oxygen in the etchant solution and it increases as time passes after the etching.\textsuperscript{57} However, the large penetration depth of light, especially in Si (~1330 nm at a light wavelength of 515 nm), hinders the selectivity of discerning the native oxide layer that has at most a 2-nm thickness without the interference of the bulk properties when using a photon pump in the photon probe configuration. Four-dimensional scanning ultrafast electron microscopy (4D S-UEM) is the sole technique capable of the surface-selective visualization of the light-triggered carrier dynamics at the angstrom-nanometer scale.\textsuperscript{48-55} 4D S-UEM uses an electron probing system to scan the top surface of the photoactive materials after being pumped with a photon pulse. As a result, secondary electrons are ejected from the top few nanometers (< 5 nm) and can be detected in real space and time. The carrier dynamics on Si surfaces have already been studied by 4D electron microscopy.\textsuperscript{56,57} However, no attention has been given to the effect of the native oxide layer on the surface charge carrier recombination.

In this work, 4D S-UEM is used to selectively study the Si surface, showing the effect of the native oxide layer (1-2 nm) in forming surface trap states that significantly further reduce the surface carrier lifetime. The uppermost surface dynamics are visualized before and after the HF-etching. In addition, the regrowth of the native oxide on HF-cleaned surface is observed by 4D S-UEM and Kelvin probe-force microscopy (KPFM). The results are of significance to understand the role of the native oxide in surface charge carrier recombination, together with the effect of etching, and represent a suitable case of study for 4D S-UEM.

A schematic representation of the principle of 4D S-UEM is given in Figure 1a. A pulsed primary electron beam is generated by a delayed UV excitation pulse (343 nm) from a cooled Schottky field-emitter tip, which generates secondary electrons (SEs) from the surface of the specimen. An
optical laser pulse (515 nm) is used to excite the surface of the material for the carrier dynamics initiation. The SEs emitted from the material surface are analyzed pixel by pixel to construct time-resolved images at various well-defined time delays between the optical pulse and the electron pulse. SE difference images are formed by referencing the frames taken at different time delays to the one taken at a very far negative time when the electron pulse arrives prior to the photon pulse. The change in the SE intensity (i.e., image contrast) in the laser-irradiated region in contrast to the non-irradiated area reveals the surface carrier dynamics together with any surface defects. Figure 1b shows time-resolved SE difference images of the Si surface with the native oxide layer (top) and the freshly stripped Si (bottom). At the far negative delays (e.g., -630 ps), no change in the

Figure 1. (a) Schematic of the 4D S-UEM principle. (b) Time-resolved SE difference images at indicated time delays for (top) pristine and (bottom) fresh HF-etched Si (100) in response to 515-nm excitation. The dashed ellipse indicates the laser footprint on the sample.
image contrast is observed, indicating the recovery of the system to the initial state after each pump–probe event. Starting from time zero (i.e., when the photon and electron pulses arrive at the same time) and at positive times (i.e., when the photon pulse arrives prior to the electron pulse), the SE intensity increases in the laser-irradiated region (dashed ellipse), resulting in a bright contrast in both samples. However, it is clear that this bright contrast fades very quickly in the pristine, as-cut Si, while it remains until the end of our time delay window (i.e., 6 ns) in the freshly etched sample.

The change in the SE intensity in the laser-irradiated region as a function of the time delay is plotted, as shown in Figure 2a. As observed in the SE difference images, the bright signal in the pristine sample rapidly disappears, indicating surface carrier trapping/recombination. The multi-lifetime components (90 ± 28 ps, 5.5 ± 1.1 ns in pristine Si) could be attributed to the oxygen defect states and surface roughness as well as the inhomogeneity present on surfaces as shown in Figure S1. In contrast, the bright contrast in the HF-etched Si shows only a rise and then a long-lived signal, indicating the absence of trapping centers in the bandgap of the Si (see Figure 1b, Figure 2a). The slow rise (180 ± 52 ps) observed in the freshly etched sample could be attributed to the diffusion of photogenerated charges to the top surface. Experiments with detailed explanation for the origin of rise components observed in different materials at different applied voltages are beyond the scope of this work and will be published elsewhere.

This difference in the charge carrier recombination can be understood in terms of the electronic structure, as shown in the schematic of Figure 2b. Typically, when the Si is excited by a green photon pulse (515 nm = 2.4 eV), an interband carrier transition takes place, and electrons are promoted from the valence band to the conduction band with a higher probability of SE emission.
Figure 2. (a) Extracted kinetics of the time-resolved secondary electron, SE, intensities of the laser footprint region in the pristine Si, freshly HF-etched Si, and Si sample one day after HF-etching and exposure to air for 5 minutes. (b) Schematic of the density of states and the excitation and trapping processes.

Therefore, the increased SE in the irradiated region results in a bright-contrast formation.\textsuperscript{58,59} However, the presence of surface defects can act as trap states for the excited electrons, leading to faster recombination.\textsuperscript{55} This appears as a fast fading away of the bright signal. Since the only difference between the pristine and the freshly HF-etched Si is the thickness of the native oxide layer, we concluded that the trap states and the ultrafast carrier recombination here are due to the silicon-oxygen bonds (thin oxide layer). The same freshly HF-etched sample was kept in the SEM vacuum chamber and then exposed to air for a few minutes, and a re-measurement of this sample confirms the effect of the native oxide layer on the surface dynamics. As shown in Figure 2a, the dynamics in the etched sample left in air for 5 minutes are in between those of the pristine and freshly etched Si. The bright signal also fades away as in the pristine sample but the dynamics are much slower (1.2 ± 0.5 ns). This means that trap states are quickly formed, although they are fewer
than in the pristine sample. This clarifies the role of the native oxide layer in the charge carrier recombination of the Si surface.

X-ray photoelectron spectroscopy (XPS) has been used extensively to study the effect of HF-etching on Si surfaces.\textsuperscript{37,41,44,60-62} However, here we investigated the surfaces of the pristine and freshly HF-etched Si by XPS to connect the information of the surface chemistry and the results obtained from the 4D S-UEM. The Si 2p, C 1s and O 1s XPS spectra of the pristine and freshly HF-etched Si are shown in Figure 3a on the same intensity axis. The Si 2p spectrum of the pristine Si shows two peaks with binding energies of 99.3 eV and 103.5 eV, which correspond to bulk Si and the surface oxide SiO\textsubscript{2}, respectively.\textsuperscript{37,40,60,61}

![Figure 3](image)

Figure 3. (a) XPS spectra of the pristine and freshly HF-etched Si with the same intensities in Si 2p, C 1s, and O 1s. (b) Illustration of the electronic structure before and directly after HF etching, showing the trapping caused by the oxygen defects.

In the freshly HF-etched Si, the SiO\textsubscript{2} peak has almost disappeared. It is worth noting that the etching process by HF, and the XPS instrument are not located at the same site, so the XPS spectra were not obtained immediately after the etching but only after a transfer time, which may result in
a slight increment in the oxidation level. The C 1s spectrum can be deconvoluted into two peaks.\textsuperscript{63} In the pristine Si, the C-C and C-H contamination peak is at 284.9 ± 0.01 eV and the C-O peak is at 286.1 ± 0.06 eV. In the freshly HF-etched Si, the C-C and C-H peak is at 284.7 ± 0.01 eV and the C-O peak is at 285.9 ± 0.07 eV.\textsuperscript{61} It is noted that the intensity of the C 1s spectrum increases after etching, which is consistent with the literature.\textsuperscript{37} One possible explanation for this is that organic impurities are adsorbed on the Si surface during the deionized water rinse after etching.\textsuperscript{37} The O 1s spectrum is composed of one peak at 532.7 eV and 532.5 eV for the pristine and freshly HF-etched Si, respectively. The oxygen peak can be due to oxygen in contamination adsorbed on the surface and/or bound to the Si as SiO\textsubscript{2}.\textsuperscript{37,64} After etching, the area under the oxygen peak is enormously reduced by 71%. Besides, the C-O peak is increased concluding that most of the left O after etching is bonded to C not Si. The XPS results together with the 4D S-UEM reveal the role of the SiO\textsubscript{2} in forming surface trap states that in turn causes faster charge carrier recombination, as illustrated in Figure 3b.

Additionally, the surface potential in the air is measured by KPFM\textsuperscript{65} to highlight the regrowth of the native oxide as the air exposure time increases, as shown in Figure 4. A green laser (523 nm) is used to study the effect of illumination on the surface potential. The freshly HF-etched sample showed an increase in the surface potential upon illumination, indicating the formation of a high concentration of photo generated carriers on the surface, as shown in Figure 4a. However, the difference in the surface potential between the dark and illuminated states decreases as the measurement is repeated on the same sample. Until approximately 40 minutes, there is no difference between the surface potential measured at laser off and on, as shown in Figure 4b, indicating the regrowth of a layer of native oxide that again changes the surface properties.
Figure 4. (a) KPFM image for freshly etched Si in the dark (bottom portion) and under the illumination of a 523-nm laser (top portion). (b) KPFM image for freshly HF-etched Si after 40 minutes from measurement in air showing no difference in the SP in the dark (bottom) and under 523-nm illumination (top). (c) Corresponding SP profiles of images (a) and (b). Optimized crystal structures and density of states (d) H-passivated Si (100)-c(4×2) and (e) O-passivated Si (100)-c(4×2) slabs calculated at HSE level of theory (the blue area indicates the surface state). (f) One-dimensional plane-average electronic charge density for holes and electrons of H-passivated Si (100)-c(4×2) (left) and O-passivated Si (100)-c(4×2) slabs (right).
This result is in good agreement with the surface dynamics in Figure 2a. Figure 4c shows the corresponding surface potentials for the KPFM images. Density functional theory (DFT) calculations are performed to study the effects of Si (100) surface terminations on the charge density properties. The hybrid functional HSE is used in our study to correct the underestimated band gaps calculated at the GGA/PBE level of theory (see more computational details in the Experimental Section). Here, we consider four Si (100)-c(4×2) slab models i) without passivation, ii) with full H-passivation, iii) with full HO-passivation, and iv) with partial O-passivation, as shown in Figure 4d-f and Figure S2 in the Supporting Information. For the clean Si (100)-c(4×2) surface without passivation, the strong surface reconstruction (i.e., Si atom inward relaxation) leads to a significant surface state with holes and electrons accumulated on the top surface layers. Similarly, when the Si (100)-c(4×2) surface is partially passivated by O atoms, it retains high localized surface charges but increased charge densities, which results in charge carrier trapping on the Si (100) surface that decreases the charge carrier lifetime. However, once the Si (100)-c(4×2) surface is fully passivated by H atoms or OH- groups, the resulting slabs show delocalized charge density features for both holes and electrons along the whole slab.

In summary, the significant impact of the ultrathin native oxide layer on the surface-carrier dynamics of Si (100) is visualized simultaneously in space and time using 4D S-UEM. Despite the ultrathin thickness (1-2 nm) of the oxide layer making it impractical to probe by time-resolved optical techniques – 4D S-UEM was able to reveal the role of the layer in forming trap states that act as quenching centers, causing ultrafast charge carrier recombination on the surface of the Si. More specifically, time-resolved SE images clearly demonstrate that the atomic surface layers of oxide control the overall charge recombination not only at the surface but also possibly at the interface of the Si absorber layer in solar cell devices. These 4D S-UEM observations were
corroborated by Kelvin probe-force microscopy, XPS, and DFT. The experimental approach described in this work presents a strategy to assess the profound influence of seemingly superficial atomic oxide layers on semiconductor materials.

EXPERIMENTAL METHODS

Materials. Silicon samples were obtained from 6 inch phosphorous-doped silicon wafers (NorSun, resistivity 1-5 Ohm/cm). The silicon samples were precleaned for 10 minutes at 90 °C in a standard piranha solution (sulfuric acid (98%) and hydrogen peroxide (30%), 3:1 by volume). To remove the native silicon oxide layer, the samples were immersed in a 5% hydrofluoric acid solution for 30 seconds and subsequently rinsed three times in deionized water. Finally, the hydrophobic surface was dried with nitrogen. The samples were then transferred directly to the S-UEM vacuum chamber for performing the 4D measurements.

4D-SUEM Experimental Setup. The 4D S-UEM setup consisted of a femtosecond laser (Clark-MXR) operating at a wavelength of 1030 nm and a pulse width of 270 fs integrated with a modified SEM (FEI Quanta 650). Briefly, the fundamental laser output was divided and directed into two independent harmonic generators (HGs) by a beam splitter to produce the second and third harmonic pulses. The output beam of the third harmonic was tightly focused on a cooled Schottky field-emitter tip (tungsten coated with zirconium oxide) to generate pulsed electrons. The second harmonic was focused on the surface of the specimen to initiate, i.e., clock, the dynamics. A computer-controlled optical delay line was used to control the relative delay between the electron and photon pulses. Upon the specimen excitation, secondary electrons (SEs) were emitted from the top surface of the sample and subsequently detected by a positively biased Everhart-Thornley detector. All experiments were done at a laser frequency of 8 MHz and accelerating voltage of 30
keV. SE images were acquired by integrating 64 frames, with a dwell time of 300 ns for each pixel. A detailed description of the 4D imaging setup is provided elsewhere.50,59

X-Ray Photoelectron Spectroscopy (XPS). XPS measurements were done using an Axis Supra DLD spectrometer (Kratos Analytical) equipped with a monochromatic Al Kα X-ray source (\(hν = 1486.6\) eV) operating at 150 W, a multichannel plate, and a delay line detector under a vacuum of ~10-9 mbar. The binding energies were referenced to the C 1s binding energy of the adventitious carbon contamination, which was taken to be 285 eV in all samples. The XPS measurements were performed on pristine samples, on which oxidation was predominant, and after HF-cleaning.

Surface Potential Measurements. Surface potential measurements were performed using scanning Kelvin microscopy and spreading resistance imaging using DCP30-type cantilevers (NT-MDT). The DCP30 is a Si cantilever with a gold-coated reflection side and a highly doped diamond coating (~100 nm thickness) on the tip side, with a resonant frequency of approximately 350 kHz and a force constant of approximately 35 N/m. Pristine and freshly etched samples were measured at room temperature in air.

Computational Methods

Density Functional Theory Calculations. Density functional theory (DFT) calculations were performed with a generalized gradient approximation (GGA)/Perdew-Burke-Ernzerhof (PBE) level using the projector-augmented wave (PAW) method, as implemented in the VASP code.66,67 For the bulk Si, a k-point grid of \(6\times6\times6\) over the Brillouin zone was used during the crystal structure optimization and electronic property calculations. The electronic plane-wave cutoff energy was set to 500 eV. The resulting lattice constant of Si was \(a = b = c = 5.468\) Å. Clean, H-terminated, HO-terminated, and O-terminated Si (100)-c(4×2) slabs were constructed to study the
surface electronic properties. All the crystal geometries were optimized until all forces on all atoms were smaller than 0.01 eV/Å. The hybrid Heyd-Scuseria-Ernzerhof (HSE) functional with an exact exchange fraction of 0.18 was used to obtain the projected density of states and charge densities for holes and electrons.

ASSOCIATED CONTENT

Supporting Information. The following files are available free of charge. Static SEM image for Si (100) as-cut wafer. Optimized crystal structures, density of states and one-dimensional plane-average electronic charge density for holes and electrons of Si (100)-c(4×2) slab models without passivation and with full HO-passivation. (PDF)

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Notes

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