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7 Room-Temperature Sputtered Nanocrystalline
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11 Nickel Oxide as Hole Transport Layer for $p-i-n$
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15 Perovskite Solar Cells
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47 **Keywords:** perovskites solar cells, room-temperature processing, sputtering, nickel oxide,
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49 conformal deposition, large area, p-i-n perovskite device configuration
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3 Nickel oxide (NiO_x) is a promising hole transport layer (HTL) for perovskite solar cells (PSCs),
4 as it combines good chemical stability, high broadband optical transparency and a high work
5 function. Excellent power conversion efficiencies (PCE) have already been reported using
6 solution-processed NiO_x . However, solution-based techniques usually require high-temperature
7 post-annealing to achieve the required HTL properties of NiO_x , which jeopardizes its use for
8 many applications, such as monolithic tandem solar cells. To resolve this issue, we developed
9 room-temperature sputtered NiO_x and demonstrated *p-i-n* PSCs with 17.6% PCE (with
10 negligible hysteresis), which is comparable to the best PSCs using sputtered and annealed NiO_x
11 without heteroatom doping. Through detailed characterization and density functional theory
12 (DFT) analysis, we explored the electrical and optical properties of the obtained NiO_x films and
13 find that they are strongly linked with the specific defect chemistry of this material. Finally, in
14 view of its use in perovskite/silicon tandem solar cells, we find that direct sputtering on random-
15 pyramid textured silicon wafers results in highly conformal NiO_x films.
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3 In the past eight years, the PCE of perovskite solar cells (PSCs) has quickly risen. Thanks to
4 their excellent electronic and optical properties such as a high absorption coefficient¹ with sharp
5 absorption onset,² photocurrents for thin-film solar cells are now already close to the theoretical
6 limit. In addition, a high charge-carrier mobility,³ long charge-carrier diffusion length⁴ as well as
7 a low Urbach energy⁵ enable a very low open-circuit voltage (V_{OC}) deficit, explaining further
8 their remarkable recent rise as a prominent photovoltaic technology. PSCs can be fabricated
9 either in $n-i-p$ or $p-i-n$ configurations, depending on the relative position of the charge
10 transport layers.⁶ Among these two device configurations, the $n-i-p$ structure has recently
11 reached record power conversion efficiencies (PCE) in excess of 23%;⁷⁻⁸ such devices are
12 usually built onto metal-oxide based electron transport layers (ETL, e.g. TiO_2 , SnO_2 , ZnO).⁹⁻¹¹
13 On the other hand, although $p-i-n$ configuration devices provides ease of fabrication, the limited
14 choice of solvent-compatible hole transport layers HTLs and the lack of efficient p -type metal
15 oxides hampers the further development of $p-i-n$ PSCs. In fact, such HTLs have been limited to
16 polymer-based HTL materials for several years (e.g. PEDOT:PSS). Nevertheless, developing a
17 high-performance HTL has significant importance for the fabrication of $p-i-n$ configuration top
18 cells in perovskite/silicon tandem solar cell applications. Replacing organic HTLs with inorganic
19 alternatives is of utmost importance to improve processing and device robustness, and also offers
20 increased versatility in choice of materials. To this end, CuI , Cu_2O , $CuSCN$, $CuPc$, and NiO_x
21 have shown promise for PSCs.¹²⁻¹⁶ Of these, >20% PCE for opaque devices has been recently
22 realized for $CuSCN$ and NiO_x .¹⁴⁻¹⁵ Both materials offer promising characteristics such as high
23 broadband transparency and a high work function (WF), resulting in a good energy-alignment
24 with the perovskite, enabling high V_{OC} . Moreover, in contrast to $CuSCN$, NiO_x chemically
25 withstands most of the solvents used in PSC fabrication. By now, NiO_x has already been studied
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3 widely with several successful reports employing solution-processing techniques such as sol-gel,
4 combustion methods, and vacuum-based depositions including sputtering, pulsed-laser
5 deposition, and atomic layer deposition.^{15, 17-19} Recently, PSCs based on thin nanoparticle films
6 of NiO_x synthesized by combustion were reported with PCEs in excess of 20%.¹⁵ However, most
7 of these studies require a high-temperature annealing step to activate the HTL properties, which
8 hinders its use on substrates with limited temperature resilience, such as polyethylene
9 terephthalate (PET). Low-temperature processing compatibility is also crucial for monolithic
10 perovskite/silicon tandem solar applications relying on bottom cells with limited temperature
11 resilience,²⁰ such as silicon heterojunction (SHJ) solar cells²¹ (which are only compatible with
12 top-cell fabrication processes up to ≈ 200 °C).²²⁻²³ Moreover, solution-based deposition
13 techniques on a random-pyramid textured silicon solar cells tend to result in poor surface
14 coverage since polymer HTLs accumulate within the valleys during annealing.²⁰ These
15 limitations motivated us to explore RF sputtering deposition at room-temperature for conformal
16 deposition on both flat and textured surfaces. To date, the PCE of planar PSCs utilizing RF-
17 sputtered NiO_x is limited to 16.2%.¹⁸ The limited conductivity, mobility, and optical
18 transparency appear currently to be roadblocks in reaching efficiencies in excess of 20%.
19 Therefore, a detailed understanding of the defect structure of NiO_x is crucial to improve its HTL
20 properties. To this end, we elucidate in this article how the defect structure of NiO_x films
21 correlates with the material's properties. Following HTL optimization, we obtained *p-i-n*
22 methylammonium lead iodide (MAPbI₃) based PSCs with PCEs of 17.6%.
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50 Regarding the properties of our NiO_x layers, figures 1.a and 1.b show the compact nature of
51 device-optimized 25 nm NiO_x films on polished quartz substrates investigated by AFM surface
52 topography and SEM surface image analysis, respectively. Figure S3 shows the conformal
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3 deposition of NiO_x films also on random pyramids textured wafers which are promising for fully
4 textured perovskite/silicon tandem solar cells. To confirm the degree of crystallinity and phase
5 analysis of the films, we performed surface-sensitive grazing incidence X-ray diffraction
6 (GIXRD) measurements since standard thin-film XRD showed no reflection in the scanned
7 region. GIXRD measurements (Figure 1.d) revealed the cubic structure of NiO_x as evidenced by
8 the peaks located at $\approx 37.2^\circ$ and $\approx 43.2^\circ$ (2θ), which represent the (111) and (200) planes,
9 respectively (JCPDS PDF No: 47-1049).²⁴ The observation of these peaks is in line with earlier
10 reports for NiO_x films deposited at low-temperature and under pure argon atmosphere.²⁴ XRD
11 analysis of step-wise annealed NiO_x films (from 100 to 700 °C, 100 °C steps, on quartz
12 substrates) demonstrated that sputtered NiO_x does not exhibit evolution of any significant peak,
13 even after annealing at high temperatures (Figure S1), which confirms the limited effect of
14 annealing on the crystallite size. The crystallite sizes (D) of the NiO_x films are calculated to be
15 9.9 nm for as-deposited NiO_x following the Scherrer formula,²⁵ which is consistent with an
16 average crystallite size of 8-9 nm as determined by AFM analysis (Figure S2).
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36 Figure 1.e confirms the high broadband transparency of our sputtered NiO_x; assuming direct
37 optical transitions, i.e. $n = 2$ in the Tauc relation $(\alpha h\nu) = B (h\nu - E_g)^{1/n}$, the optical band gap is
38 calculated to be 3.55 eV for optimized NiO_x films, which is very close to the previously reported
39 results (Figure S4).²⁶⁻²⁷ Notably, annealing of the NiO_x films at 500 °C results in a decrease in
40 optical transparency (Figure S5), indicating an increasing presence of interstitial O atoms within
41 the NiO_x lattice.^{28,29} Increasing process temperature or post-annealing process usually results in
42 decreased Ni/O concentration ratio in the resultant films.³⁰⁻³¹ This compositional change might
43 be responsible for the decreased conductivity of the NiO_x films after post-annealing process
44 (Table S2). We should note that, the concentration of the microstructural defects of the as
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deposited films has a strong impact on the electrical conductivity of the post-annealed samples. Therefore, by controlling the defect chemistry of the as deposited films, post-annealing process can be eliminated.

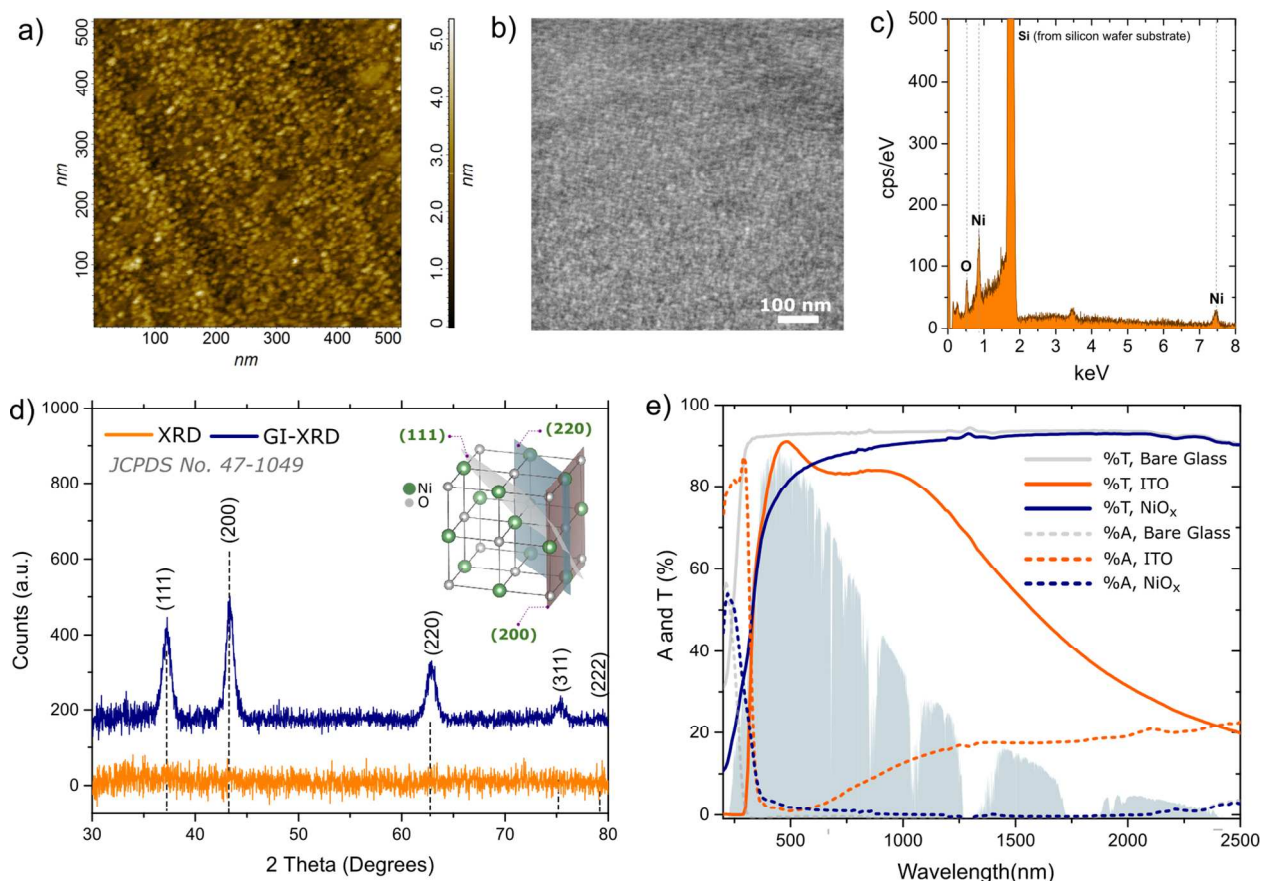


Figure 1. a) AFM surface topography, b) SEM surface image, c) EDX spectra, d) XRD analysis (The inset figure shows the prominent crystallographic planes of NiO_x), and e) transmittance (T), and absorbance (A) spectra of the sputtered NiO_x films measured with integrating sphere. The shaded gray area shows the spectral irradiance for AM 1.5 conditions. Absorbance was determined from 100-T-R.

The composition and defect density of the NiO_x films determines their optoelectronic properties and hence device performance. In this regard, elemental quantification energy-dispersive X-ray spectroscopy (EDX) analysis was performed on NiO_x films deposited on pre-etched silicon wafers to prevent any O contribution from the substrate. EDX analysis reveals on average 56% O and 44% Ni atomic ratios, suggesting O-rich nature of the films (Figure 1.c). The sputtering of

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2
3 the NiO_x films was performed in a pure argon atmosphere to avoid further oxidation of the films.
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5 The dependency of the sputtering yield to the atomic mass of the target (atomic mass of oxygen
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7 (O) and nickel (Ni) is 15.99 and 58.69 u, respectively) may explain the observed Ni vacancy
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9 (V_{Ni}) and O interstitial (O_i) and therefore the existence of Ni³⁺ ions in the structure. X-ray
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11 photoelectron spectroscopy (XPS) spectra of the films are given in Figure 2.a, showing Ni 2p_{3/2}
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13 peaks in NiO_x. These spectra typically feature a main peak at ≈854 eV, shoulder at ≈856 eV, and
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15 shake-up satellite peak at ≈862 eV.³²⁻³⁵ The peak centered at a binding energy of 853.7 and 855.6
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17 eV can be ascribed to Ni²⁺ which is characteristic of the standard Ni–O octahedral bonding
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19 configuration of the cubic NiO_x rock salt and a vacancy induced Ni³⁺ ion, respectively.³⁶ The
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21 broad peak centered at 860.5 eV is attributed to shake-up.³⁷⁻³⁸ The corresponding high-resolution
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23 O 1s scans are shown in Figure 2.b. The spectra show a narrow peak at a lower binding energy of
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25 529.5 eV, which arises from octahedral bonding of Ni–O, and a broader component at 531.2 eV.
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27 This likely stems from defect sites within the NiO_x crystal lattice and may be ascribed to O
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29 interactions with a Ni-deficient lattice and nickel-hydroxides, including defective NiO with
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31 hydroxyl groups adsorbed onto the surface.^{33-34, 39-40} The coexistence of Ni²⁺ and Ni³⁺ states is in
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33 line with the non-stoichiometric nature of the NiO_x film; their compositional ratio can be
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35 calculated by integrating the respective area for Ni³⁺ and Ni²⁺ on XPS spectra. The area ratio of
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37 1.66 confirms the O-rich structure of the films. The Ni³⁺ in the structure forms a small portion of
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39 Ni₂O₃ within the film. As this phase is not distinguishable on the XRD graphs, we hypothesize it
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41 structurally to be amorphous.⁴¹ Calculated DFT formation energies of Ni and O vacancies (1.18
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43 eV and 4.40 eV, respectively) show that Ni vacancies are easier to form than O vacancies,
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45 agreeing with our experimental results that NiO is metal deficient. Hence, the *p*-type character of
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47 NiO is governed by the Ni vacancies, acting as acceptors. After formation of a Ni vacancy, the
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structure relaxes and the nearest O/Ni atoms around the Ni vacancy move outwards/inwards (see Figure 2.d) as observed previously.⁴² According to the density of states (DOS) shown in Figure 2.e, Ni vacancies in bulk NiO result in energy levels near the top of the valence band (shallow acceptor levels). These states enhance the *p*-type character (hole concentration). We suggest that this process results in increased hole conductivity.

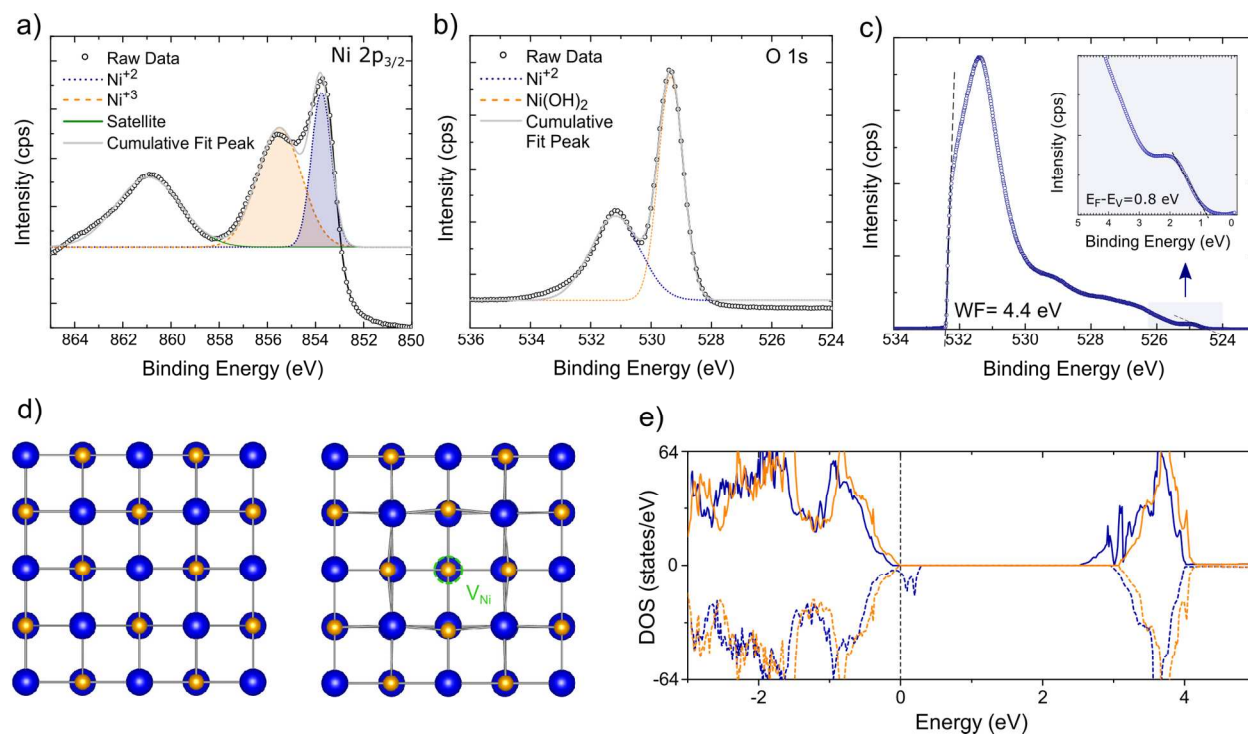


Figure 2. XPS survey spectra of a) Ni $2p_{3/2}$ and b) O $1s$ states for sputtered NiO_x films. c) UPS spectra of NiO_x films. The inset figure shows a detailed view of the valence band edge. d) Atomic models for pristine (NiO) and defective (NiO_x) crystals ($\text{Ni}_{31}\text{O}_{32}$ which corresponds to 3.2% (i.e. $1/31$) Ni vacancy doping, or $\text{Ni}:\text{O} = 31/32 = 0.97$). e) Spin-up (solid lines) and spin-down (dashed lines) DOS of pristine ($\text{Ni}:\text{O} = 32/32$ orange lines) and defective ($\text{Ni}:\text{O} = 31/32$, blue lines) NiO . The energy zero is set to the valence band maximum or Fermi level.

Conduction in NiO_x films occurs via localized Ni^{3+} states.⁴³⁻⁴⁴ Thus the carrier mobility is associated with the density of these localized states. The measured Hall mobility of the films is $0.52 \text{ cm}^2/(\text{Vs})$, which is in the expected range for pure-Ar sputtered NiO_x films (Table S2).⁴⁵

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3 Such a relatively high hole mobility compared to other successful *p*-type materials underlines
4 their promising HTL character (see the comparison in Table S3).
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9 To evaluate the performance of solar cells featuring sputtered NiO_x films, we fabricated *p-i-n*
10 PSCs in the structure of ITO/NiO_x/MAPbI₃/PCBM/BCP/Ag, using 6,6-Phenyl C₆₁ butyric
11 acid methyl ester (PCBM) and bathocuproine (BCP) back electrodes (see Figure 3.a). We
12 optimized the properties of the NiO_x layer towards high device performance by engineering
13 the sputtering parameters (Figure S8). The stacked layers of the optimized device were
14 analyzed by cross-sectional SEM, resulting in layer thicknesses of ~120 nm, ~25 nm, ~350
15 nm, ~60 nm, and ~100 nm for ITO, NiO_x, MAPbI₃, PCBM, and Ag, respectively. The
16 thickness of the BCP layer is <5nm and undetectable in SEM (see Figure 3.b). Top view
17 SEM, XRD, and UV-vis-NIR measurements (Figure S6) were also carried out to probe the
18 formation of smooth and homogenous MAPbI₃ crystal thin films on NiO_x layers. Figure S7.a
19 (Figure S7.b) shows the SEM image of a typical pinhole-free and uniform perovskite film on
20 glass (on NiO_x). The corresponding XRD spectra of the perovskite thin film (Figure S7.c)
21 shows no obvious PbI₂ or other non-perovskite phases, further confirming the film quality.
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40 Figure 3.d depicts the J–V curve of the champion device, which has a PCE of 17.60% with a
41 short-circuit current density $J_{SC} = 20.65 \text{ mA cm}^{-2}$, $V_{OC} = 1.078 \text{ V}$ and $FF = 0.79$, measured
42 under AM1.5G conditions. The integrated photocurrent of 20 mA cm^{-2} from the EQE spectra
43 matches well with the J–V results (Figure 3.c and 3.e). As shown in Figure 3.f, devices retain
44 their initial efficiency over 10 minutes when held at their maximum power point in nitrogen
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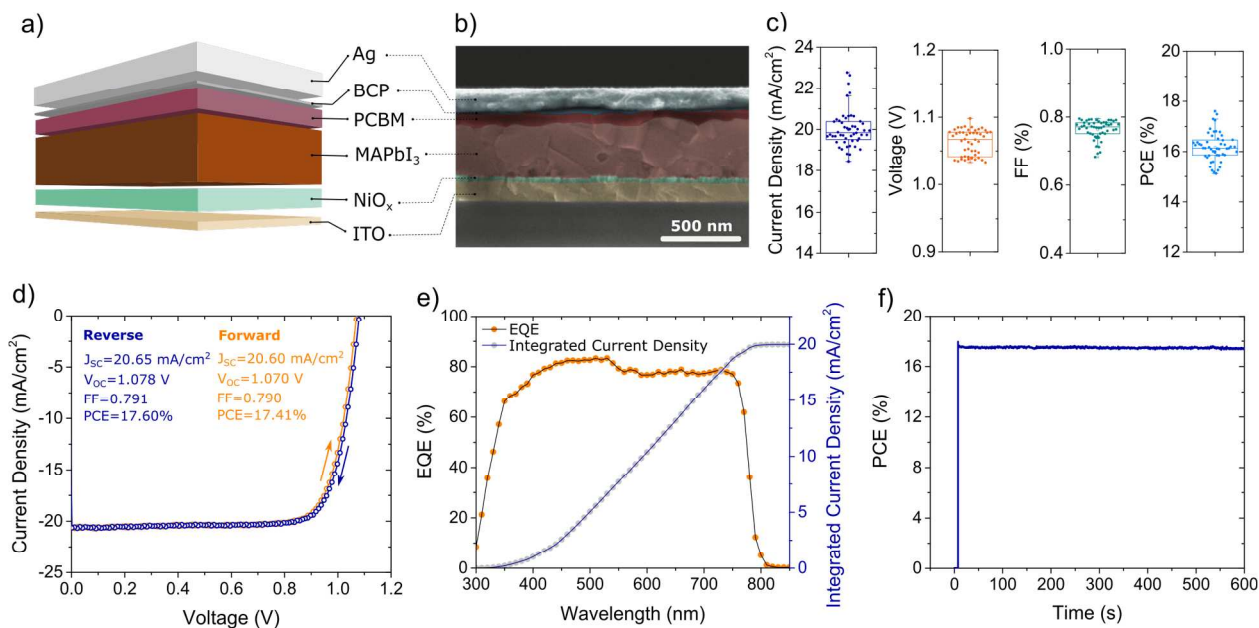


Figure 3. a) Schematic structure of the fabricated PSC with NiO_x as HTL. b) Cross-sectional SEM image of the device showing the individual layers. c) The statistical distribution of the device characteristics for 50 devices fabricated in different batches and associatively d) J–V characteristics of the champion device with optimized parameters. e) EQE spectra together with integrated current density. f) Stabilized efficiency of the champion device by J_{SC} tracking with time at a constant bias of 0.89 V with the controlled temperature at 25 °C. Note that all devices were tested at a 50 mV/s scan rate.

To investigate the carrier-recombination kinetics in MAPbI_3 in the presence of sputtered NiO_x layers, we performed time-resolved photoluminescence spectroscopy (TRPL). Figure 4.a shows 2D pseudo-color PL spectra of standalone MAPbI_3 films and $\text{MAPbI}_3/\text{NiO}_x$ junctions. As depicted in Figure 4.a, the perovskite thin films show the characteristic PL peak at 771 nm without any additional spectral feature. Carrier lifetimes following pulsed laser excitation at 650 nm were obtained by a single-exponential fit (Figure 4.b). From the fit of the PL decays, we find that the reference MAPbI_3 sample has PL lifetime of 135 ns, while the PL lifetime of the $\text{MAPbI}_3/\text{NiO}_x$ junction is reduced to 92 ns. As the V_{OC} and photocurrent values of the sputtered- NiO_x based perovskite device are high, we may argue that the reduction in PL lifetime indicates efficient charge extraction and not increased non-radiative recombination.^{46–48} To further

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3 investigate the charge-carrier transport dynamics and their effect on device performance,
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5 transient photocurrent (TPC) measurements were performed.
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9 The dependency of the PCE and hysteresis response of the devices shown in Figure 4.c which
10 reveals that NiO_x HTL based devices achieve the performance independently of the J–V scan
11 speed over the range of 10 – 2000 mV/s. Transient photocurrent (TPC) measurements that use a
12 speed over the range of 10 – 2000 mV/s. Transient photocurrent (TPC) measurements that use a
13 step function for the optical excitation provided insight on carrier trapping and the effect of traps
14 on carrier transport. Between the turn-on and turn-off dynamics, steady-state conditions were
15 reached for over 100 μs. At early microsecond times, we observe non-monotonic turn-on
16 dynamics with (recombination-active) increased light intensity, which might be correlated with
17 traps at the electrode interface. However, the photocurrent reaches steady-state within 80 μs in
18 all cases, and after turn-off of the light pulse decays within ~5 μs independent of the light
19 intensity which demonstrates the limited influence of such trap states (Figure 4.d).
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33 Ideally, HTLs should not induce an energetic barrier to the holes at the perovskite interface (due
34 to a valence band offset, Δ_{VB}), whereas the conduction band offset (Δ_{CB}) should be sufficiently
35 high to block electrons. To analyze these band offsets, the valence band maximum (V_{BM}) of the
36 NiO_x films was determined by ultraviolet photoelectron spectroscopy (UPS). From the UPS cut-
37 off spectra in Figure 2.c, we determine the work function of NiO_x to 4.4 eV, which is similar to
38 literature-reported values.^{18, 49} As shown in the inset of Figure 2.c, V_{BM} of the NiO_x is
39 determined to be –5.20 eV, while the conduction band minimum (C_{BM}) is –1.65 eV when
40 considering $E_g = 3.55$ eV. The energy-level diagram shows that MAPbI₃ (–5.43 eV) has a
41 positive Δ_{VB} with sputtered NiO_x, equal to 0.23 eV (Figure 4.f). This is sufficiently small to
42 achieve a low contact resistivity between NiO_x and MAPbI₃ without any significant effect on the
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J–V curve.⁵⁰ Meanwhile, NiO_x provides efficient electron blocking properties owing to a sufficiently high Δ_{CB} at its interface with the V_{BM} of MAPbI₃ (−3.93 eV). We also calculated the band offsets by DFT using the electrostatic potential line-up method (Figure 4.e).⁵¹ We obtain $\Delta_{VB} = 0.21$ eV, which is consistent with the UPS results, and $\Delta_{CB} = 2.16$ eV, confirming the electron-blocking properties of NiO_x. Figures 4.e and 4.f illustrate the DFT and experimental band offsets, respectively.

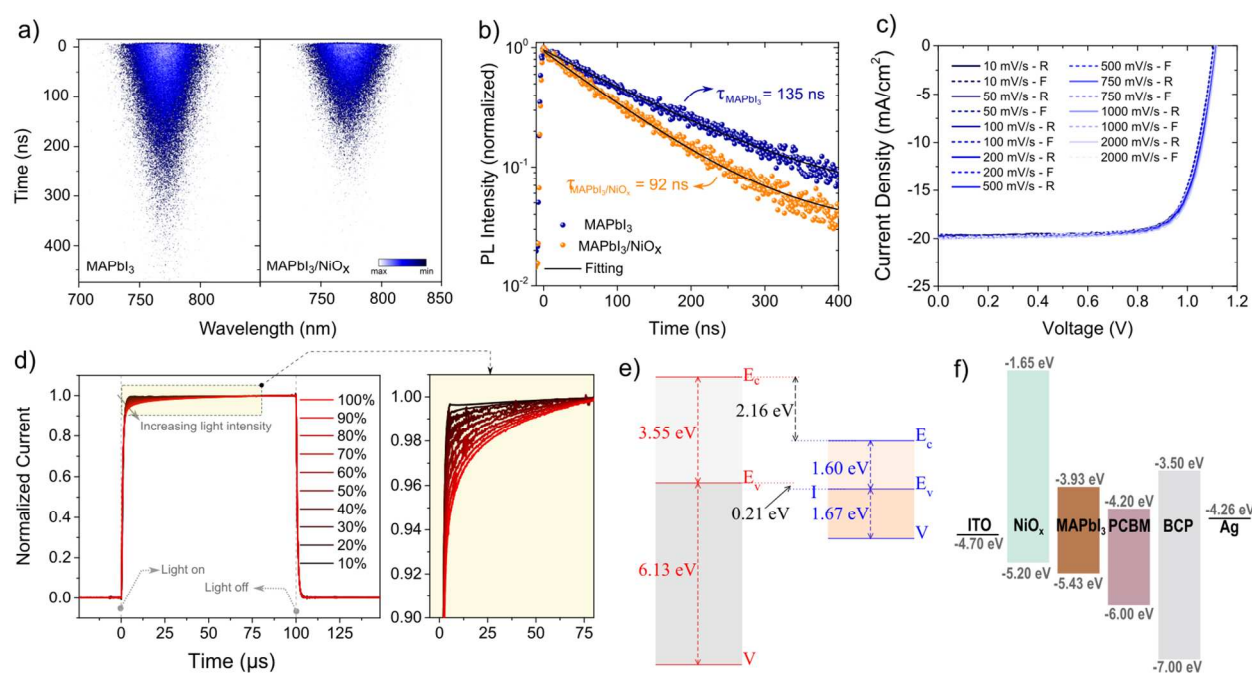


Figure 4. a) Streak camera images of MAPbI₃ and MAPbI₃/NiO_x samples deposited on quartz substrates. The images show the evolution of the spectra with a time of the samples excited with 650 nm wavelength (fluence = 49 nJ/cm², laser frequency = 250 kHz). b) TRPL decay of the related samples probed at 771 nm. The initial carrier concentration after pulsed excitation was calculated as $n_0 = 8 \times 10^{15}$ cm⁻³. c) J–V characteristics of the devices with different scan speeds varying from the ultra-slow and ultra-fast scan under standard AM1.5G solar illumination (100 mW cm⁻²). d) Transient photocurrent behavior of the devices measured with a white LED at 100 μ s square pulse together with a detailed view of rising and decaying periods. e) Valence and conduction band offsets (black numbers). V is the macroscopic average electrostatic potential of the bulk system. f) Energy levels of the stacked materials.

In summary, we provided a simple and effective fabrication recipe for all ≤ 100 °C processed *p–i–n* PSCs with 17.6% champion efficiency and negligible hysteresis, which is among the

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3 highest reported for sputtered NiO_x without heteroatom doping, and irrespective of the applied
4 thermal budget. In addition, we showed the potential application of room-temperature sputtered
5 NiO_x films with perovskite/silicon tandem solar cells by depositing conformal layers on random
6 pyramids textured wafers. We revealed that the electrical and optical properties of the optimized
7 films are strongly connected to the defect chemistry of the NiO_x. For better HTL performances,
8 the relatively low electrical conductivity and carrier concentration of the sputtered NiO_x can
9 be increased by heteroatom doping. Previously, doping of sputtered NiO_x by cobalt and
10 magnesium has been reported by elsewhere.^{49, 52} Doping with other heteroatoms such as copper,
11 zinc, silver may increase the hole transporting properties of NiO_x. We should note that although
12 doping is beneficial for increased conductivity and mobility, optically it is usually accompanied
13 with parasitic absorption losses potentially resulting in reduced photocurrents on the device
14 level. Further progress of the PSC with sputtered NiO_x may be realized by contact passivation,
15 and employing alternative ETLs that provide more suitable band alignment. Room-temperature
16 processing of NiO_x opens a new avenue for the processing of organic and perovskites solar cells
17 on readily available cost-efficient polymer foils and other temperature sensitive substrates. In
18 conclusion, improved understanding of defect chemistry and the doping mechanism of the NiO_x
19 offers new opportunities to improve the PCE with less parasitic absorption and enhanced charge
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47 ASSOCIATED CONTENT

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50 Supporting Information. Experimental parameters and DFT calculation details. XRD graphs of
51 the annealed NiO_x thin films. Grain size and roughness analysis by AFM. SEM images of the
52 NiO_x films on textured silicon wafers. $(\alpha h\nu)^2$ vs $h\nu$ graph of the NiO_x films. Comparison of
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3 transmittance and absorbance spectra of the as-deposited and annealed NiO_x films.
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5 Transmittance, absorbance and reflectance spectra of the MAPbI₃ films. SEM surface
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7 morphology and XRD analysis of the MAPbI₃ films. The solar cell characteristics with different
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9 deposition conditions of NiO_x. Planar and macroscopic average electrostatic potentials. The table
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11 comparing the PCE of the devices with literature.
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46 Notes

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48 The authors declare no competing financial interest.
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TOC GRAPHICS

